

Diamond:H/Transition Metal Oxides Transfer-Doping: Efficiency and Transistor Performance

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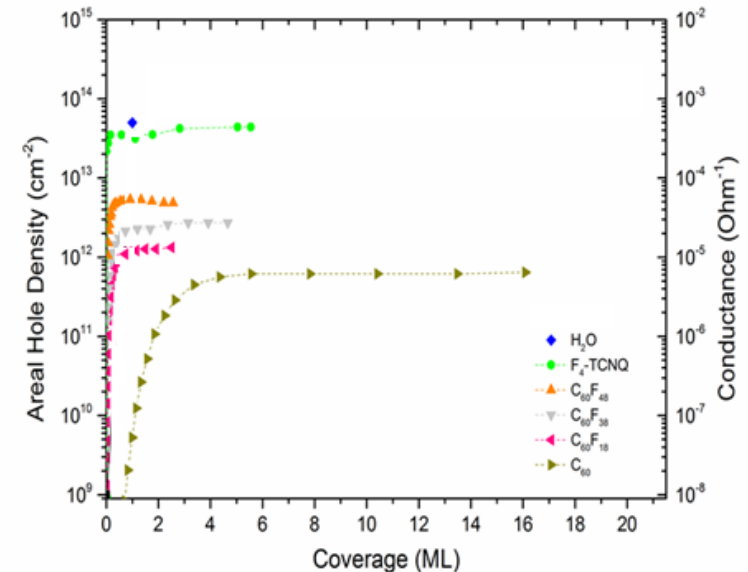
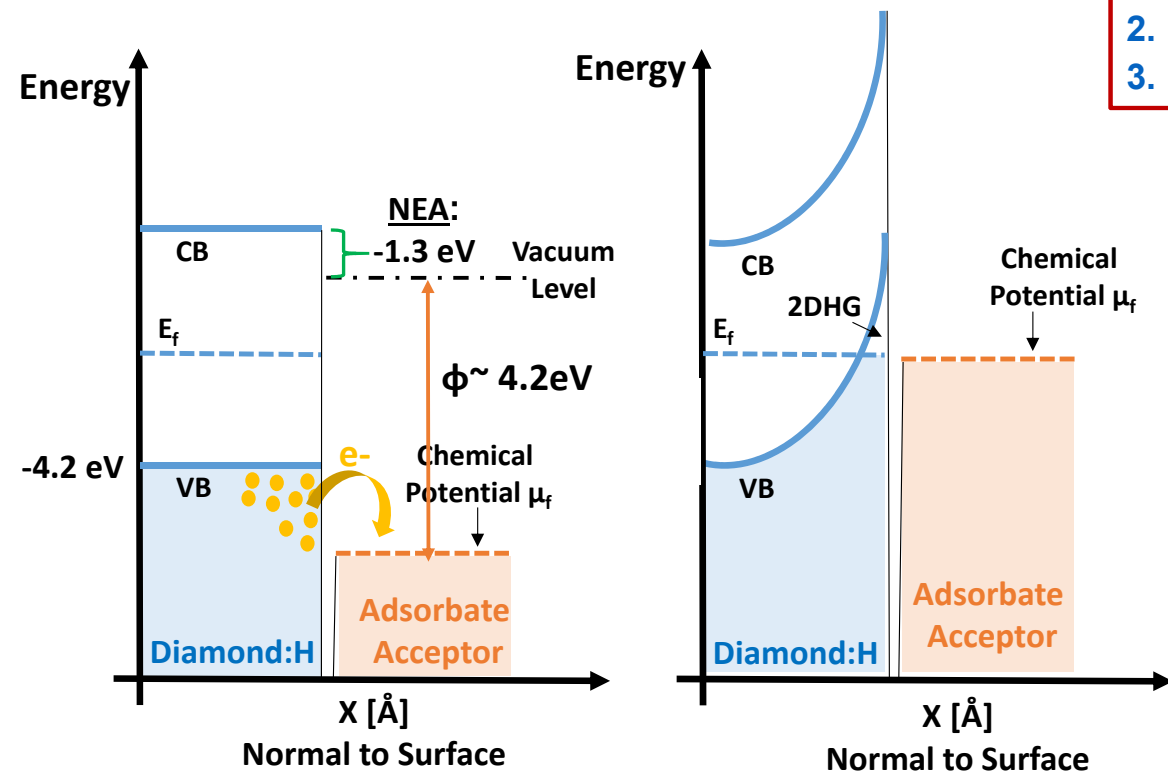


Diamond Surface Transfer Doping with Adsorbates Molecules

Before
Electron Transfer

After
Equilibrium State

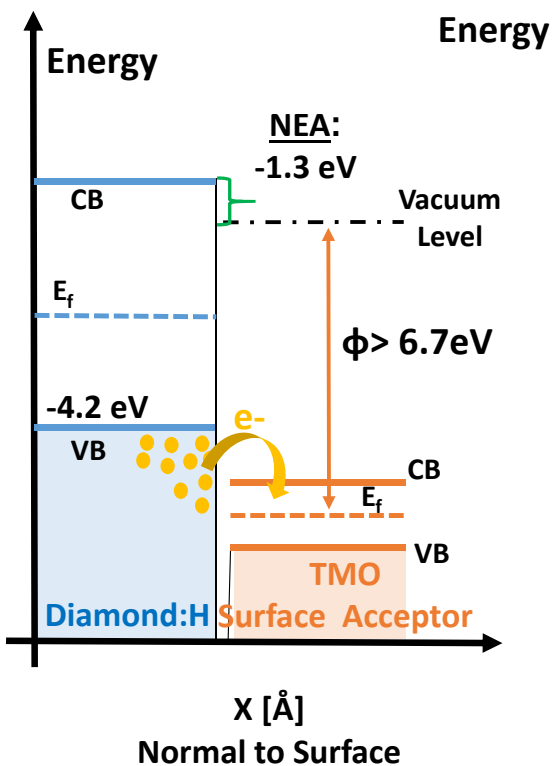
- Drawbacks:**
1. Volatile and Sensitive to Atmospheric Fluctuations.
 2. No Temperature Stability.
 3. Low Work Function \rightarrow Limited conductivity



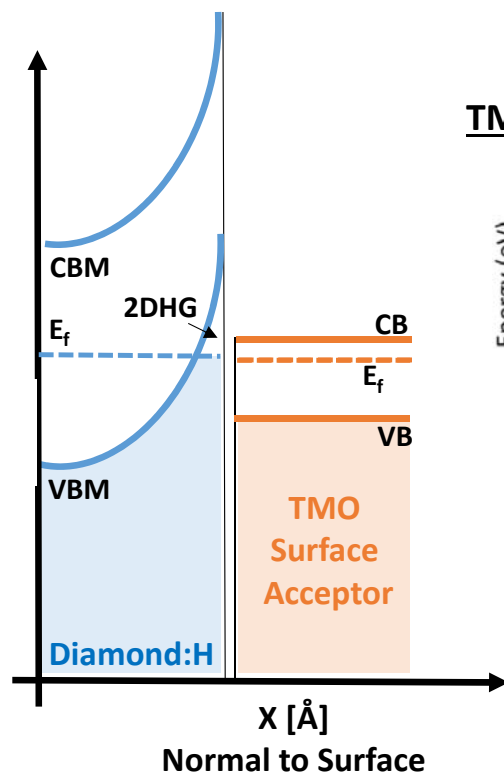
Strobel et.al Nature,430, (2004) ; W. Chen, Prog. Surf. Sci (2009)

Diamond Surface Transfer Doping with Transition Metal Oxides

Before
Electron Transfer



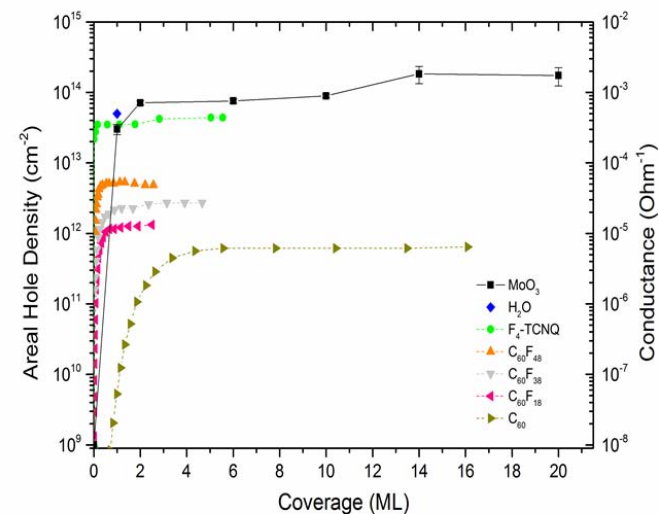
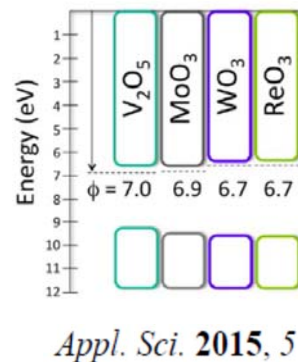
After
Equilibrium State



Advantages:

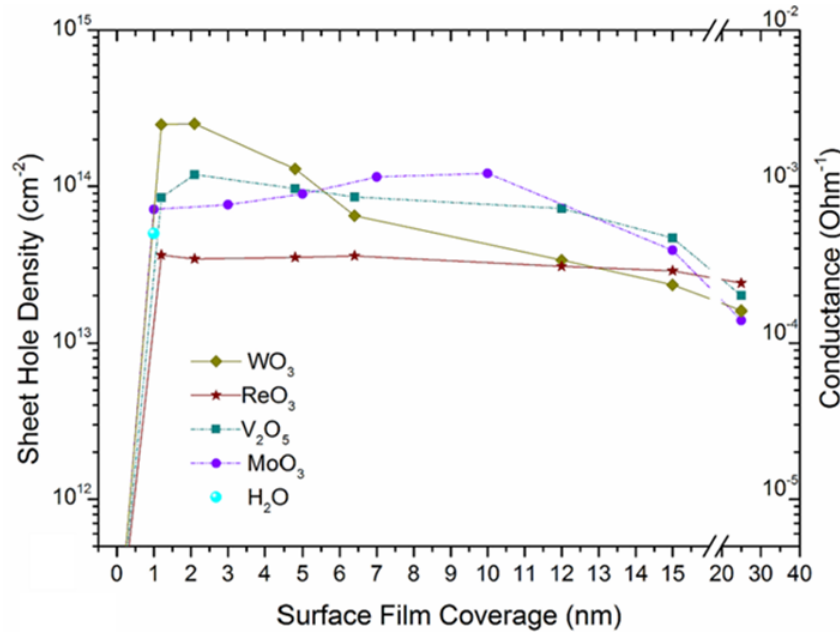
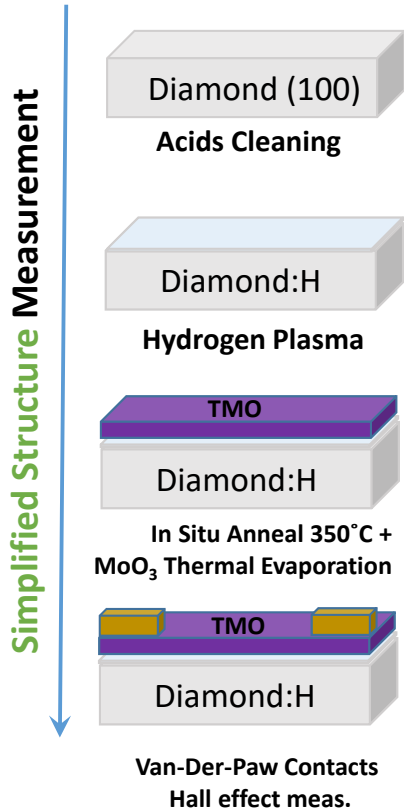
1. Temperature Stability (up to 350-450°C).
2. Higher Work function \rightarrow Higher conductivity.

TMOs Surface Acceptors:



Tordjman et. al. *Advanced Materials Interfaces*, 201300155, (2014).

Diamond:H/TMO Transfer Doping



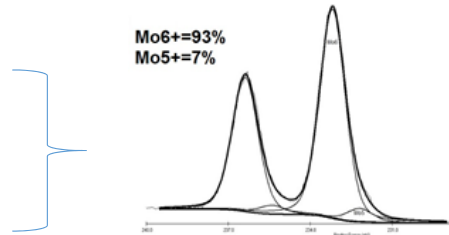
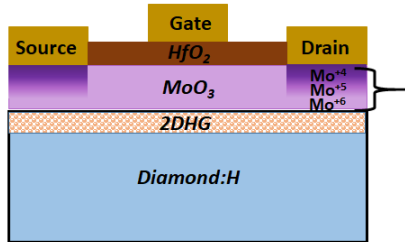
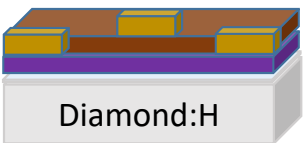
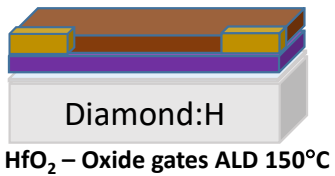
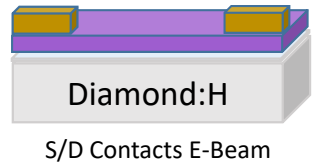
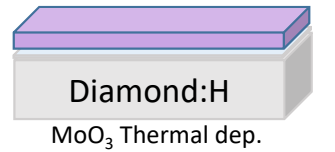
TMO	ReO ₃	MoO ₃	WO ₃	V ₂ O ₅
Crystalization Phase Cubic, cP4 Hexagonal Orthorhombic Monoclinic Orthorhombic (330-740C) Monoclinic (17-330C) Triclinic (-50-17C)	3.742	3.96x13.85x3.69	7.30x7.54x3.84	11.51x3.55x4.37
Density [g/cm³] 6.92 4.69 7.16 3.357	6.7	6.9	6.7	7
Work Function [eV]				

TMOs come into Various:

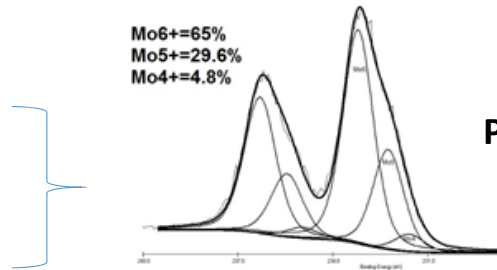
1. **Crystallization Structures.**
2. **Oxidation phases. (i.e. MoO_{3-x}, V₂O_{5-x}, WO_{3-x} etc..)**
3. **Coverage Uniformity.**

MoO₃ Thermal Evaporation Integrity to FET Fabrication Process

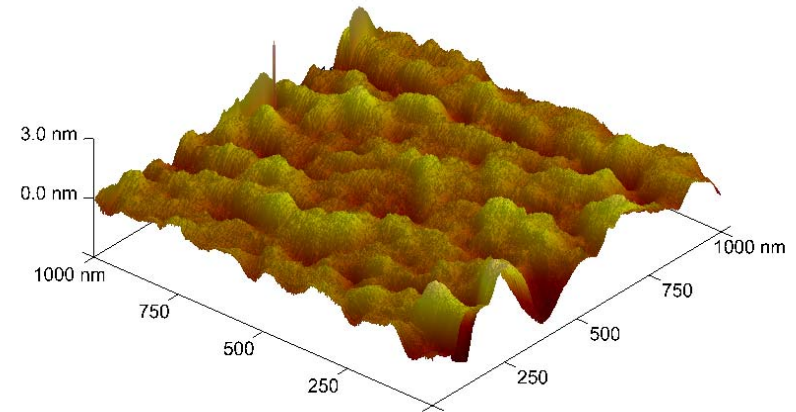
Low Budget Temp. FET Fab. Process



$$P_s = 1 \times 10^{14} \text{ cm}^{-2}$$

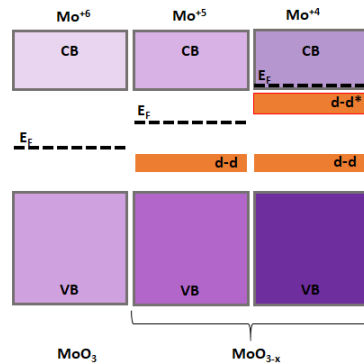


$$P_s = 4 \times 10^{12} \text{ cm}^{-2}$$



Ra=0.86nm

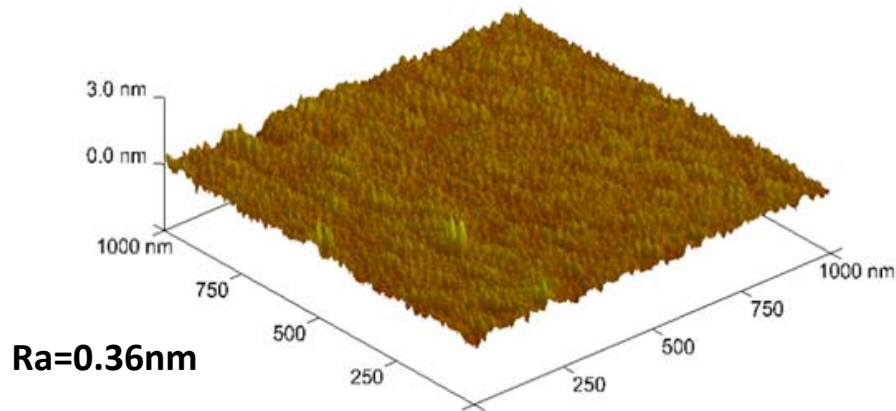
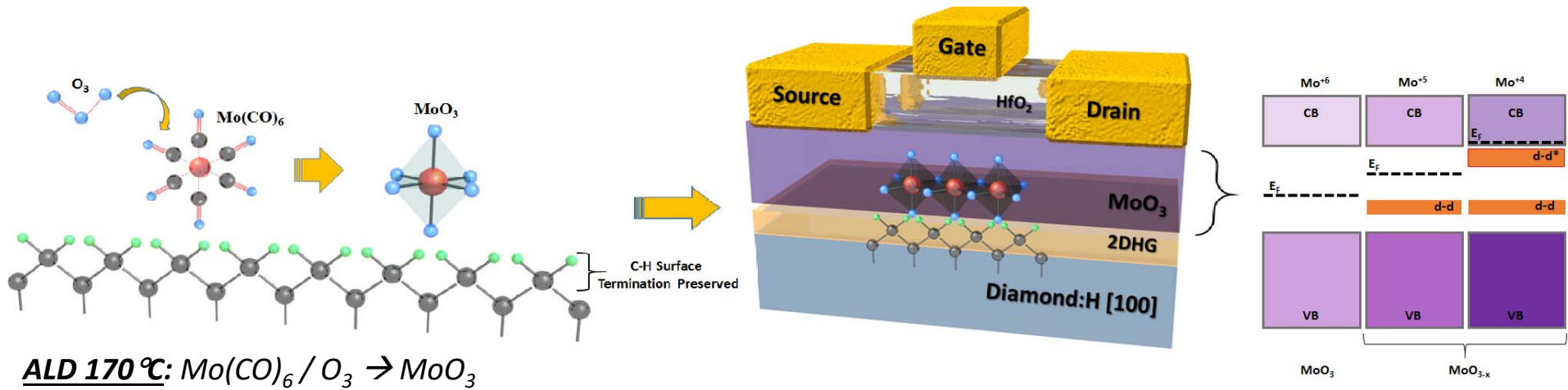
$\phi : 6.7\text{eV} \rightarrow 5\text{eV}$



Challenges:

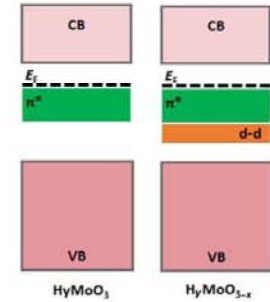
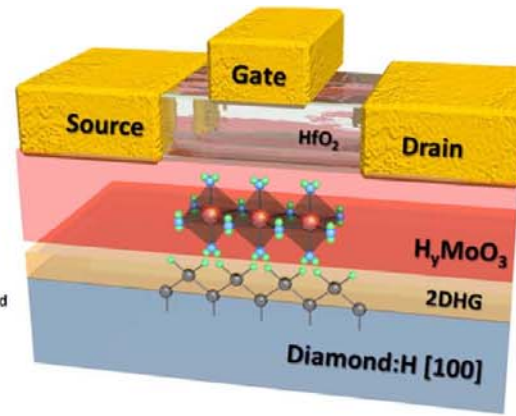
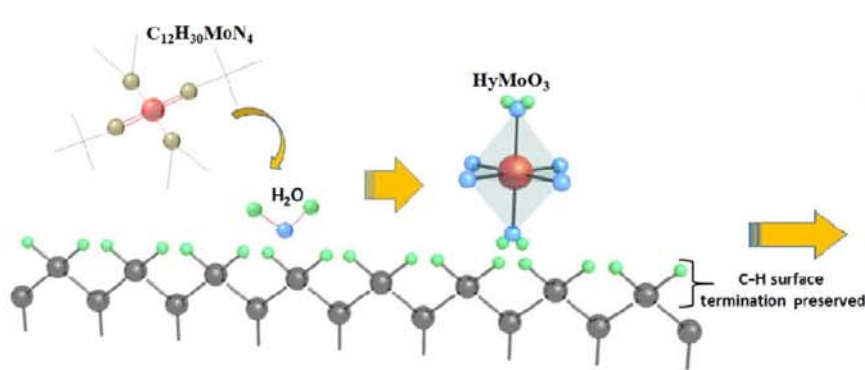
1. Nonhomogeneous Morphology.
2. Stoichiometry Changed by Fab. Process.
3. Carrier Loss due to band-energy Misalignment.

ALD MoO₃ Surface Acceptor

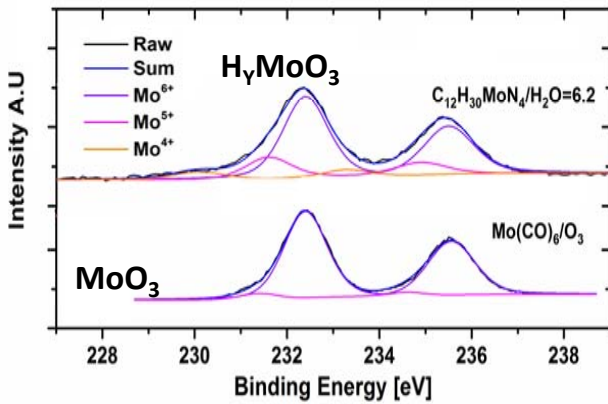


- ✓ Roughness Quality improved.
- 1. Electronic Gap- States- reducing WF .
- 2. Band Energy Misalignment.

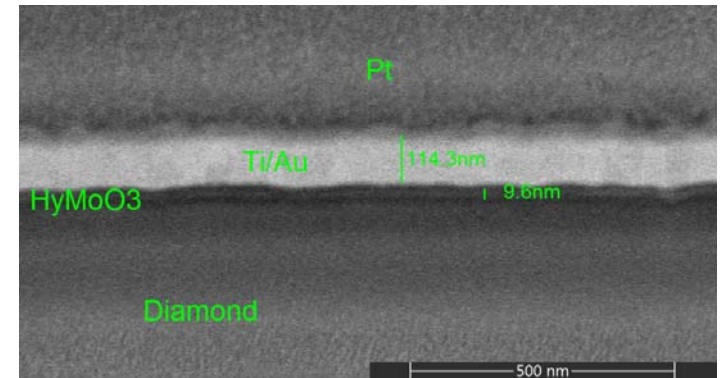
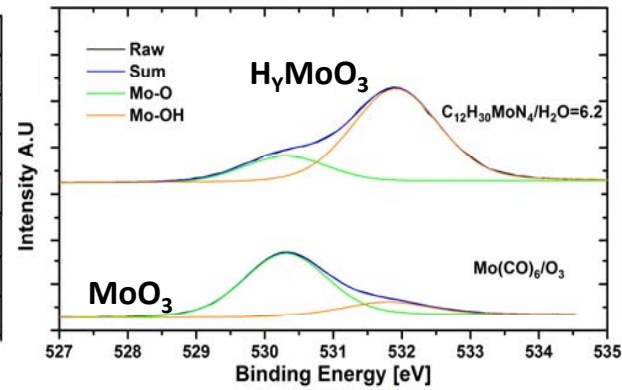
ALD H_yMoO_3 Surface Acceptor



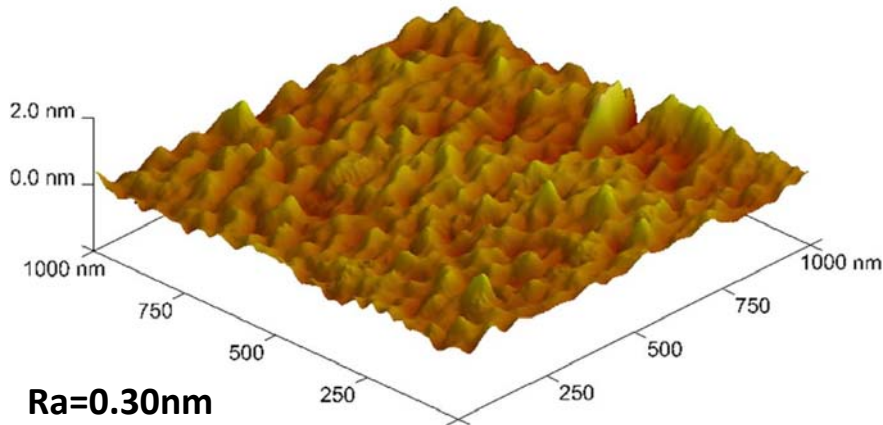
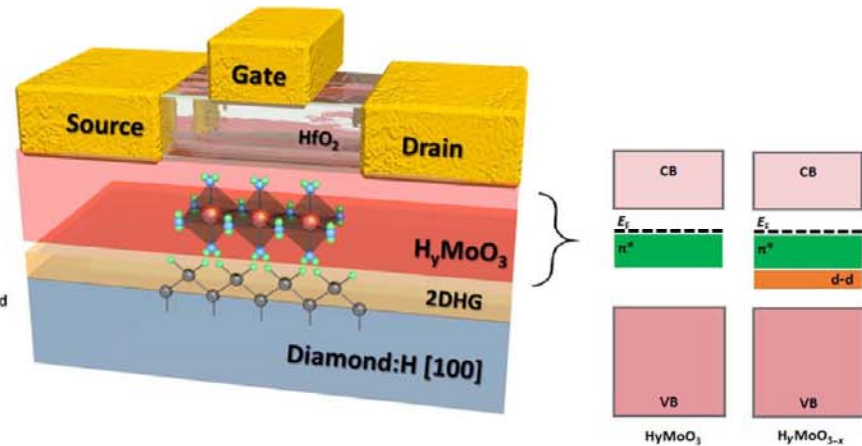
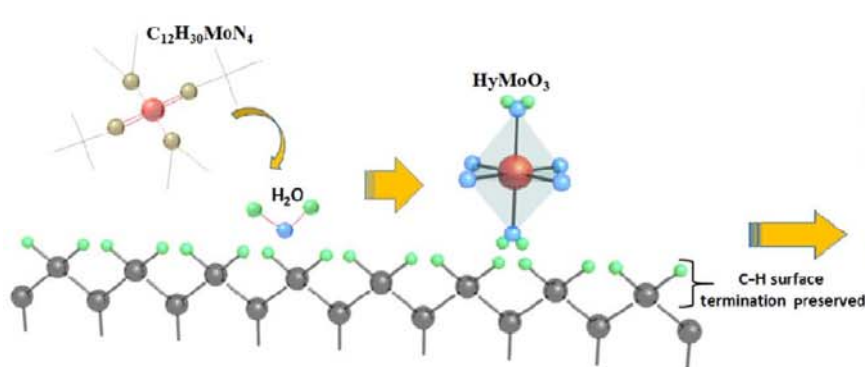
Mo3d XPS



O1s XPS



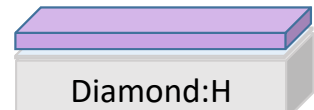
ALD H_yMoO_3 Surface Acceptor



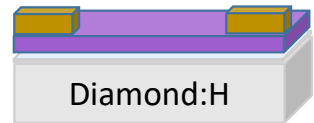
- Hydrogen Incorporation Contributes:**
- 1. Strengthen Covalent bonds.**
 - No O Reduction.
 - No Work Function Degradation.
 - 2. Improved Surface Roughness Quality.**
 - What about Energy- Band Alignment?

Diamond:H/MoO₃ Vs. H_yMoO₃ Properties

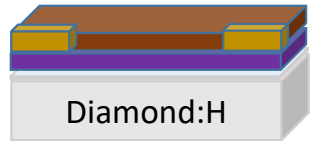
High Budget Temp. FET Fab. Process



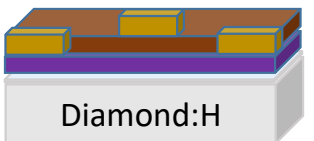
MoO₃ or H_yMoO₃ ALD



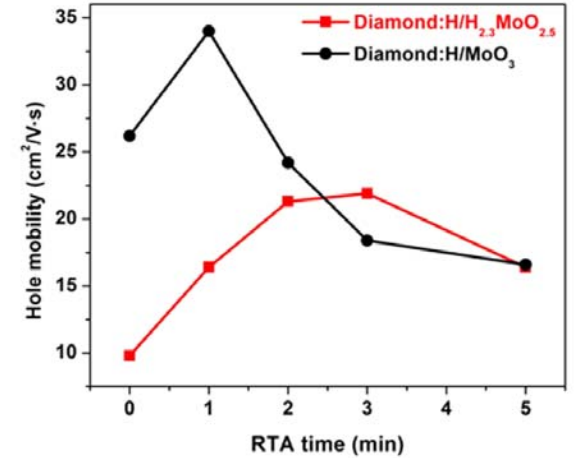
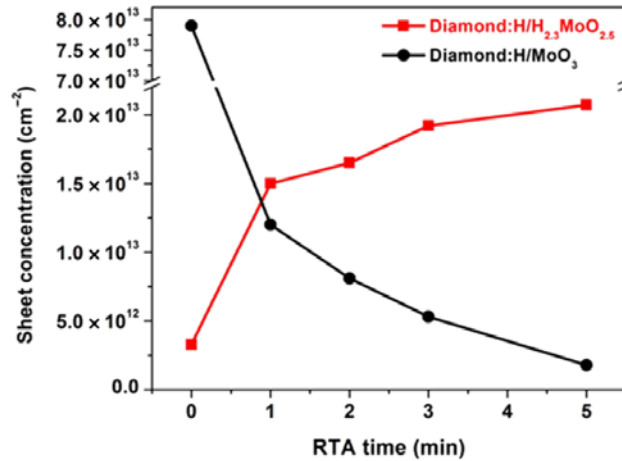
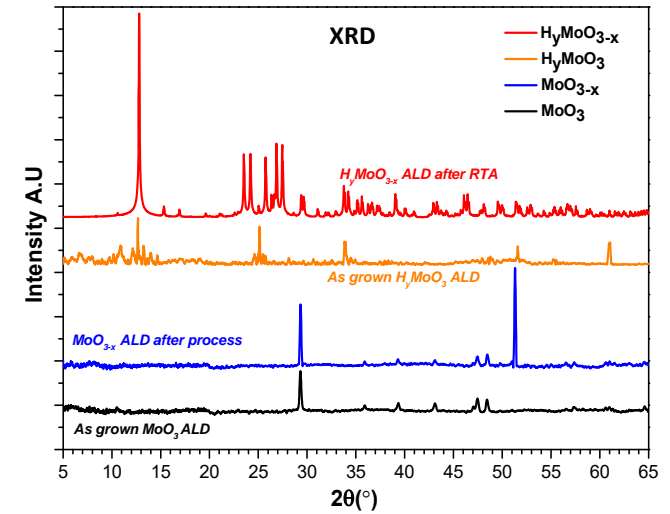
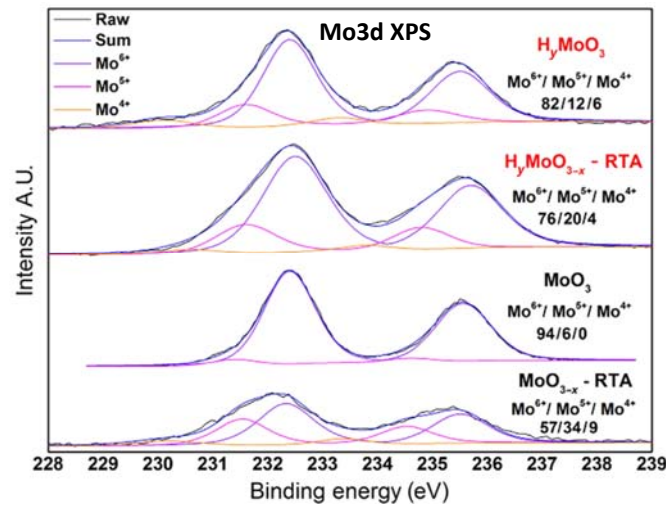
S/D Contacts E-Beam



HfO₂ – Oxide gates ALD 150°C
+ RTA 600°C

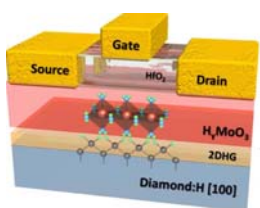


Top gate + Channel isolation

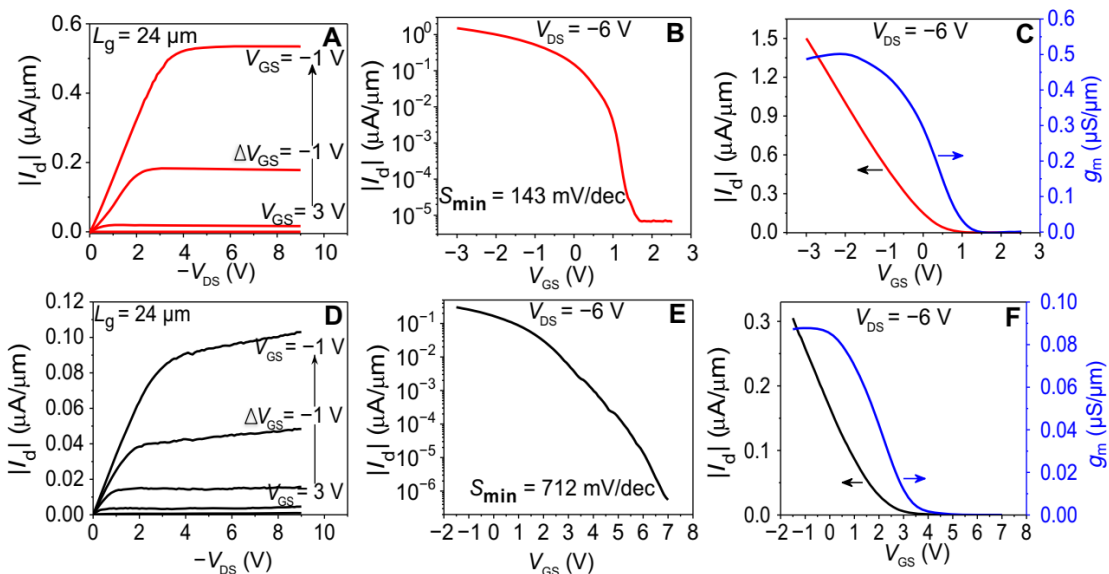
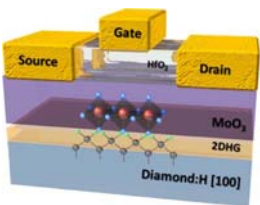


Diamond:H/MoO₃ Vs. H_yMoO₃ FETs

H_yMoO₃

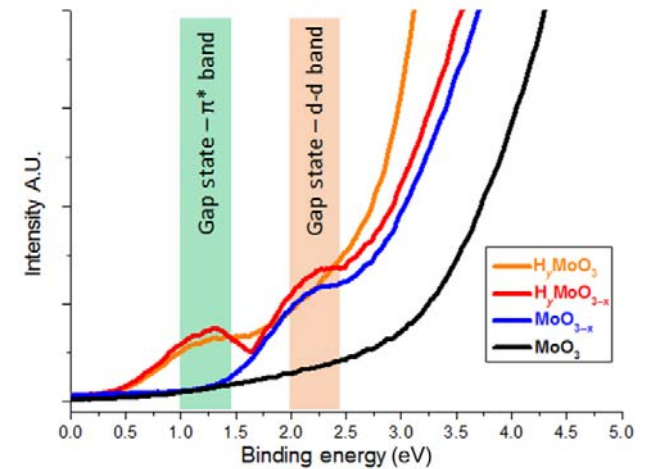
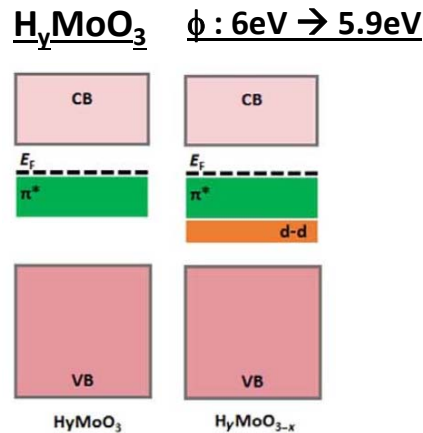
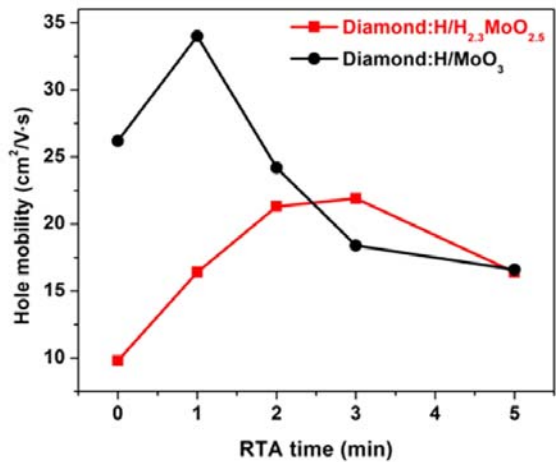
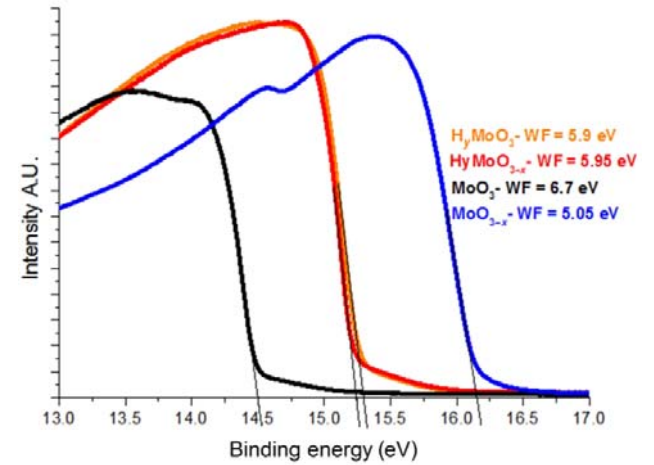
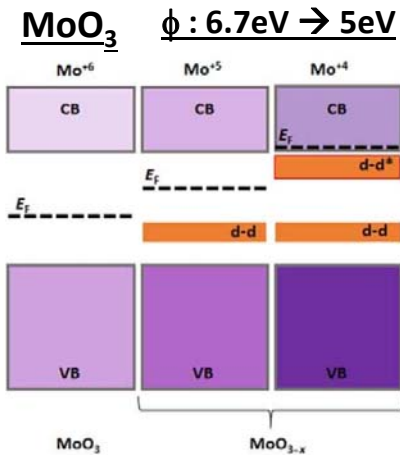
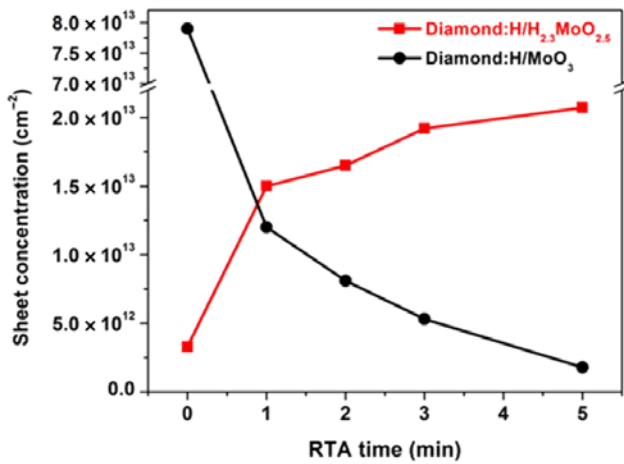


MoO₃

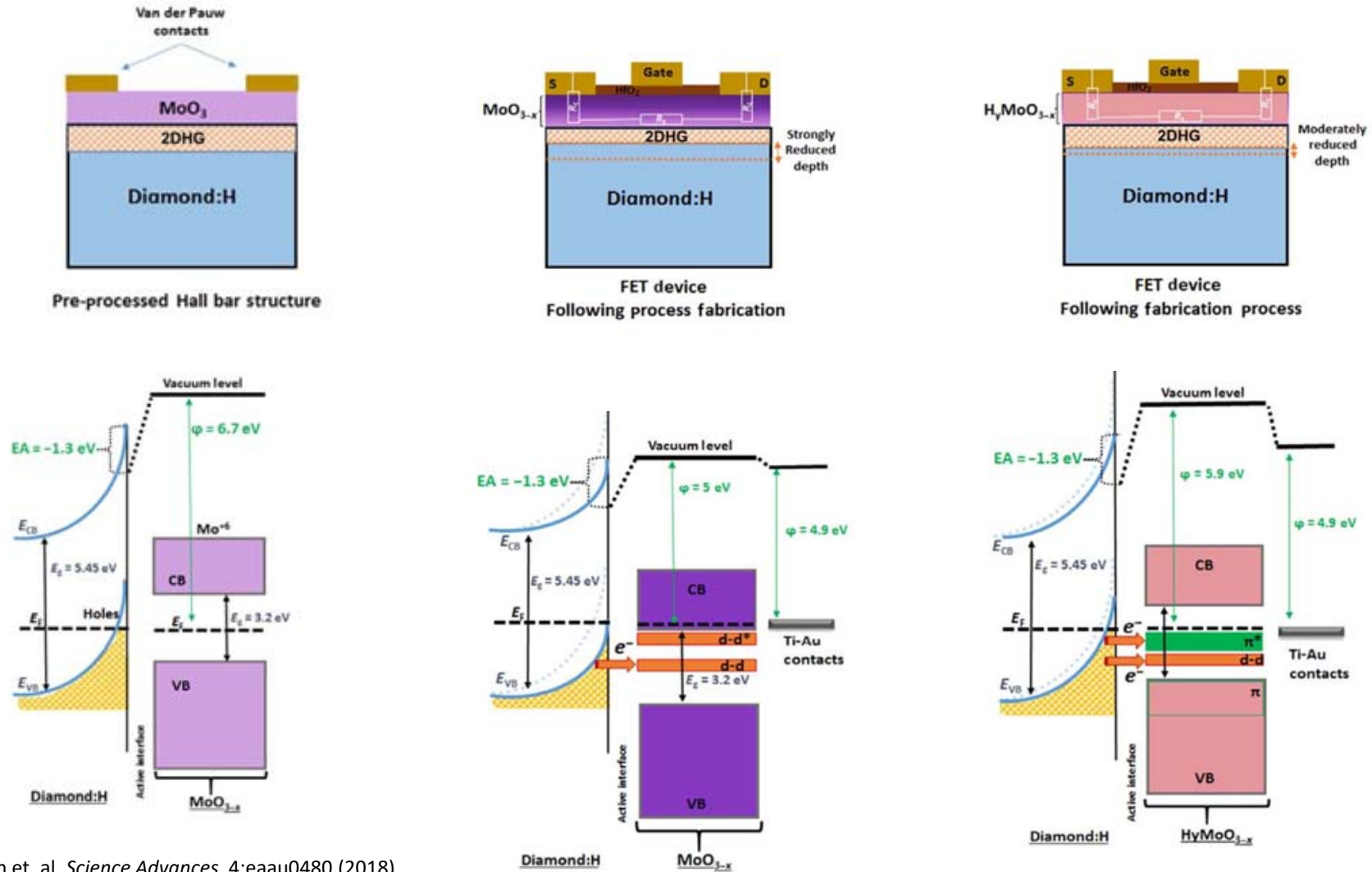


Configuration	FET							Hall		
	Completed device - Post-process fabrication							Pre-processed structure		
	Hole mobility ($\text{cm}^2/\text{V}\cdot\text{s}$)	Hole concentration (cm^{-2})	Sheet resistance ($\text{k}\Omega/\text{sq}$)	Contact resistance ($\text{k}\Omega\cdot\mu\text{m}$)	Maximum drain-current ON/OFF ratio	Maximum transconductance ($\mu\text{S}/\mu\text{m}$)	Minimum subthreshold swing (mV/dec)	Hole mobility ($\text{cm}^2/\text{V}\cdot\text{s}$)	Hole concentration (cm^{-2})	Sheet resistance ($\text{k}\Omega/\text{sq}$)
Diamond:H/MoO ₃	1.7	3.2×10^{12}	260	75	2.7×10^4	0.09	712	26.2	7.9×10^{13}	3.02
Diamond:H/H _y MoO _{3-x}	20.2	5.1×10^{12}	43	11	2.1×10^5	0.5	143	22.4	1.9×10^{13}	15

MoO₃ Vs. H_yMoO₃ Band-Energy Alignment



MoO₃ Vs. H_yMoO₃ Band-Energy Alignment



Conclusions

- A Novel Advantageous Surface Acceptor: H_yTMO
- General Strategy for Integrating and Modulating Electronic States in H_yTMO .
- **Diamond:H/ H_yMoO_3** Surface Acceptor shows:
 1. **Improved Morphology Smoothness.**
 2. **Immunity to Harsh Processing FET Fab.**
 3. **Improved Cross-Transport via band-energy alignment.**

Thank You

